

Application No. 10/612,733
37 C.F.R. § 1.312 amendment

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Amendments to the Specification:

Please replace paragraph [0001] with the following amended paragraph:

[0001] This application claims the benefit of U.S. Provisional Application No. 60/400,849, filed August 02, 2002, the U.S. Provisional Application No. 60/422,922, filed October 31, 2002, and the U.S. Provisional Application 60/424,083, filed November 5, 2002, all of which are incorporated herein by reference in their entireties and for all purposes. This application is related to the following U.S. Patent Applications: U.S. Application No. 10/360,005, filed February 7, 2003 now Patent No. 6,753,561; U.S. Application No. 10/330,512, filed December 26, 2002 now Patent No. 6,753,561; Application No. 10/330,153, filed December 26, 2002 now Patent No. 6,834,008; Application No. 10/330,964, filed December 26, 2002 now Patent No. 6,831,854; Application No. 10/330,170, filed December 26, 2002 now Patent No. 6,970,375; Application No. 10/330,900, filed December 26, 2002 now Patent No. 6,850,429; Application No. 10/330,150, filed December 26, 2002 now Patent No. 6,798,685; Application No. 10/330,965, filed December 26, 2002 now Patent No. 6,850,455; and the applications that are titled "RAM With Multiple Memory Layers Using Shared Logic," Application No. 10/612,191, filed July 1, 2003 now Patent No. 6,906,939 "Line Drivers That Fit Within A Specified Line Pitch," Application No. 10/613,099, filed July 1, 2003 now Patent No. 6,836,421 "Cross Point Memory Array With Fast Access Time," Application No. 10/612,776, filed July 1, 2003; and "Line Drivers That Use Minimal Metal Layers," Application No. 10/612,263, filed July 1, 2003. all of which are filed on date herewith. All of the above applications are hereby incorporated herein by reference in their entireties and for all purposes.